Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application. The following listing provides the amended claims with the amendments marked with deleted material in [brackets] and new material underlined to show the changes made.

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Listing of Claims:

27.   (Original) A method of routing a plurality of nets in a region of an integrated circuit ("IC") layout, each net having a set of pins in the region, the method comprising:

a)   partitioning the region into several sub-regions, wherein a plurality of edges exist between said sub-regions,

10   b)   for each particular net, identifying an edge-intersect probability for each particular edge that specifies the probability that a set of potential routes for the particular net will intersect the particular edge, wherein a potential route for a particular net traverses the set of sub-regions that contain the particular net's set of pins; and

c)   using the identified edge-intersect probabilities to identify routes for the

15   nets.

28.   (Original) The method of claim 27, wherein, for each particular net, the edge-intersect probability for each particular edge equals the number of potential routes of the particular net that intersect the particular edge divided by the number of potential routes of the particular net.

20   29.   (Original) The method of claim 28, wherein identifying the edge-intersect probabilities for each particular net comprises:

a)   identifying the set of sub-regions that contain each particular net's pins;

b)   based on each particular net's identified set of sub-regions, retrieving the particular net's edge-intersect probabilities from a storage structure.

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30.   (Original) The method of claim 27, wherein identifying the edge-intersect probabilities comprises:

for each particular net:

a)   identifying the set of potential routes for the particular net;

b)   for each particular edge, computing the number of potential routes of the particular net that intersect the particular edge;

c)   dividing the computed number of each particular edge by the number of potential routes of the particular net.

31.   (Original) The method of claim 30, wherein identifying the set of potential routes for each particular net comprises retrieving the set of routes from a storage structure.

32.   (Original) The method of claim 30, wherein identifying the set of potential routes for each particular net comprises generating the set of routes after partitioning the IC region.

33.   (Original) The method of claim 27 further comprising:

for each particular edge, computing a sum of the probabilities identified for the particular edge for all the nets;

using the summed probabilities for the edges to predict congestion of the edges;

routing the nets based on the predicted congestion of the edges.

34.   (Original) The method of claim 27, wherein using the identified probabilities to identify routes for the nets comprises:

a)   using the edge-intersect probabilities to predict congestion of the edges;

b)   based on the predicted congestion, identifying routes for nets.

35.   (Original) The method of claim 27, wherein using the identified probabilities to identify routes for the nets comprises:

a)   using the edge-intersect probabilities to derive an edge-intersect cost for

each edge;

b) using the potential routes and the edge-intersect costs to formulate a linear-programming ("LP") problem;

c) solving the LP problem to identify one route for each net.

36. (Original) The method of claim 35, wherein the LP problem is an integer linear programming ("ILP") problem, and solving the ILP problem results in an ILP solution that specifies one route for each net.

37. (Original) The method of claim 35, wherein solving the LP problem results in a real-number solution for each net, wherein using the identified probabilities to identify routes for the nets further comprises converting the real-numbered solutions to integer solutions that specify only one route for each net.

38. (Currently Amended) A method of routing a plurality of nets in a region of an integrated circuit ("IC") layout, each net having a set of pins in the region, the method comprising:

a) partitioning the region into several sub-regions, wherein a plurality of paths exist between said sub-regions,

b) for each particular net, identifying a_an—path-use probability for each particular path that specifies the probability that a set of potential routes for the particular net will use the particular path, wherein a potential route for a particular net traverses the set of sub-regions that contain the particular net's set of pins; and

c) using the identified path-use probabilities to identify routes for the nets.

39. (Original) The method of claim 38, wherein, for each particular net, the path-use probability for each particular path equals the number of potential routes of the particular net that use the particular path divided by the number of potential routes of the particular net.

40. (Original) The method of claim 39, wherein identifying the path-use probabilities for each particular net comprises:

a) identifying the set of sub-regions that contain each particular net's pins;

b) based on each particular net's identified set of sub-regions, retrieving the particular net's path-use probabilities from a storage structure.

41. (Original) The method of claim 38, wherein identifying the path-use probabilities comprises:

for each particular net:

a) identifying the set of potential routes for the particular net;

b) for each particular path, computing the number of potential routes of the particular net that use the particular path;

c) dividing the computed number of each particular path by the number of potential routes of the particular net.

42. (Original) The method of claim 41, wherein identifying the set of potential routes for each particular net comprises retrieving the set of routes from a storage structure.

43. (Original) The method of claim 41, wherein identifying the set of potential routes for each particular net comprises generating the set of routes after partitioning the IC region.

44. (Original) The method of claim 38 further comprising:

for each particular path, computing a sum of the probabilities identified for the particular path for all the nets;

using the summed probabilities for the paths to predict congestion of the paths;

routing the nets based on the predicted congestion of the paths.

45. (Original) The method of claim 38, wherein using the identified probabilities to identify routes for the nets comprises:

a)   using the path-use probabilities to predict congestion of the paths;

b)   based on the predicted congestion, identifying routes for nets.

46.   (Original) The method of claim 38, wherein using the identified probabilities to identify routes for the nets comprises:

a)   using the path-use probabilities to derive an path-use cost for each path;

b)   using the potential routes and the path-use costs to formulate a linear-programming ("LP") problem;

c)   solving the LP problem to identify one route for each net.

47.   (New) A computer readable medium that stores a computer program for routing a plurality of nets in a region of an integrated circuit ("IC") layout, each net having a set of pins in the region, the computer program comprising sets of instructions for:

a)   partitioning the region into several sub-regions, wherein a plurality of edges exist between said sub-regions,

b)   for each particular net, identifying an edge-intersect probability for each particular edge that specifies the probability that a set of potential routes for the particular net will intersect the particular edge, wherein a potential route for a particular net traverses the set of sub-regions that contain the particular net's set of pins; and

c)   using the identified edge-intersect probabilities to identify routes for the nets.

48.   (New) The computer readable medium of claim 47, wherein, for each particular net, the edge-intersect probability for each particular edge equals the number of potential routes of the particular net that intersect the particular edge divided by the number of potential routes of the particular net.

49.   (New) The computer readable medium of claim 28, wherein the set of instructions

for identifying the edge-intersect probabilities for each particular net comprises sets of instructions for:

    a)   identifying the set of sub-regions that contain each particular net's pins;

    b)   based on each particular net's identified set of sub-regions, retrieving the particular net's edge-intersect probabilities from a storage structure.

50.   (New) A computer readable medium that stores a computer program for routing a plurality of nets in a region of an integrated circuit ("IC") layout, each net having a set of pins in the region, the computer program comprising sets of instructions for:

    a)   partitioning the region into several sub-regions, wherein a plurality of paths exist between said sub-regions,

    b)   for each particular net, identifying a path-use probability for each particular path that specifies the probability that a set of potential routes for the particular net will use the particular path, wherein a potential route for a particular net traverses the set of sub-regions that contain the particular net's set of pins; and

    c)   using the identified path-use probabilities to identify routes for the nets.

51.   (New) The computer readable medium of claim 50, wherein, for each particular net, the path-use probability for each particular path equals the number of potential routes of the particular net that use the particular path divided by the number of potential routes of the particular net.

52.   (New) The computer readable medium of claim 51, wherein the set of instructions for identifying the path-use probabilities for each particular net comprises sets of instructions for:

    a)   identifying the set of sub-regions that contain each particular net's pins;

    b)   based on each particular net's identified set of sub-regions, retrieving the particular net's path-use probabilities from a storage structure.

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